

ABSTRACT OF THE DISCLOSURE

5 In a flash memory write mode, a microcomputer operation mode setting circuit sets a mode setting signal at one level. At this stage, a voltage drop caused by an LPF formed of a resistor, an inductor, and a capacitor can be suppressed at a low level. In a mode other than the flash memory write mode, the microcomputer operation mode setting circuit sets the mode setting signal at another level. At this stage, high frequency noise can be removed by the LPF formed of the resistor, the inductor, and the capacitor.